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High Efficiency Power Amplifier for non Constant Envelop Digital Modulation

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ABSTRACT

The aim of this project is to develop high efficiency power amplifier for non constant envelop digital modulation. The power amplifier specifically aims to provide high power efficiency to all modulation scheme supported by TETRA 2 standard. More precisely project aims to design a highly linear and power efficient power amplifier for all modulation in TETRA 2 standard. The expected result of the research can also be used for other mobile handsets and wires access devices, SDR (software defined radio) terminals, and especially useful for mobile handset in navy.