

# **EBRAM - Extending the BlockRAMs in FPGAs to support caches and hash tables in an efficient manner**

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## **Abstract:**

In this paper we discuss how a typical BlockRAM in an FPGA can be extended to enable the implementation of more efficient caches in FPGAs with very minor modifications to the existing BlockRAM architectures. In addition, the modifications also allow other components, such as hash tables, to be implemented more efficiently.