

# Embedded Conference Scandinavia, Stockholm, October 2009

*Invited talk*

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## **Abstract:**

A programmable BBP is one of the essential enablers of future mobile unit. As wireless standards evolve, the processing power needed for baseband processing increases dramatically and the underlying hardware needs to cope with various standards or even simultaneously maintaining several radio links. Meanwhile, the acceptable power consumption in mobile terminals is strictly limited. These challenges require both system and architecture level innovations.

I am going to introduce the design methodology for radio BBP and challenges and implementations of radio BBP for future mobile units. The ASIP BBP architecture will be presented here as an example of a BBP design aimed at reducing power and silicon cost while maintaining sufficient flexibility

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